# 230607 - SBMIC - Systems Based on Microprocessors

**Coordinating unit:** 230 - ETSETB - Barcelona School of Telecommunications Engineering  
**Teaching unit:** 710 - EEL - Department of Electronic Engineering  
**Academic year:** 2019  
**Degree:** MASTER'S DEGREE IN ADVANCED TELECOMMUNICATION TECHNOLOGIES (Syllabus 2019). (Teaching unit Optional)  
MASTER'S DEGREE IN ELECTRONIC ENGINEERING (Syllabus 2013). (Teaching unit Optional)  
MASTER'S DEGREE IN TELECOMMUNICATIONS ENGINEERING (Syllabus 2013). (Teaching unit Optional)  
**ECTS credits:** 5  
**Teaching languages:** English

## Teaching staff

**Coordinator:** JORDI SALAZAR  
**Others:** MANEL DOMÍNGUEZ, VICENTE JIMÉNEZ

## Degree competences to which the subject contributes

### Specific:

1. Ability to use programmable logical devices, as well as to design analog and digital advanced electronics systems. Ability to design communication devices, such as routers, switches, hubs, transmitters and receivers in different bands.  
2. Ability to develop electronic instrumentation, as well as transducers, actuators and sensors.

### Transversal:

3. EFFECTIVE USE OF INFORMATION RESOURCES: Managing the acquisition, structuring, analysis and display of data and information in the chosen area of specialisation and critically assessing the results obtained.  
4. FOREIGN LANGUAGE: Achieving a level of spoken and written proficiency in a foreign language, preferably English, that meets the needs of the profession and the labour market.

## Teaching methodology

- Lectures  
- Application classes  
- Laboratory classes  
- Individual work (distance)  
- Exercises  
- Other activities  
- Short answer test (Control)  
- Extended answer test (Final Exam)

## Learning objectives of the subject

Learning objectives of the subject:

The aim of this course is to provide students with an understanding of microprocessor-based systems and their use in instrumentation, control and communication systems. Topics covered in the course include, microprocessor architecture and organization, bus architectures, memory and I/O subsystems, timing and interfacing, peripheral controllers and programming in the 'C' language.
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Learning results of the subject:

- To know how microprocessor based systems can be applied.
- To understand the architecture and operation of a microprocessor system.
- To be able to use a microprocessor development system to prepare and run a program.
- To be able to implement microprocessor-based systems, including both hardware and software, using a specific microprocessor or microcontroller.

<table>
<thead>
<tr>
<th>Study load</th>
<th>Hours large group: 26h</th>
<th>%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Total learning time: 125h</td>
<td>20.80%</td>
<td></td>
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<tr>
<td>Hours medium group:</td>
<td>0h</td>
<td>0.00%</td>
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<tr>
<td>Hours small group:</td>
<td>13h</td>
<td>10.40%</td>
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<tr>
<td>Guided activities:</td>
<td>0h</td>
<td>0.00%</td>
</tr>
<tr>
<td>Self study:</td>
<td>86h</td>
<td>68.80%</td>
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</table>
# Content

<table>
<thead>
<tr>
<th>Section</th>
<th>Description</th>
<th>Learning time</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>1. Introduction</strong></td>
<td>Course description. Programmable electronic systems. Architecture of a microprocessor system.</td>
<td>16h</td>
</tr>
<tr>
<td><strong>2. The CPU</strong></td>
<td>ALU, registers, control unit and datapath. Instruction sets: RISC and CISC. Bus cycles</td>
<td>20h</td>
</tr>
<tr>
<td><strong>3. Interfacing memory</strong></td>
<td>Types of memory: ROM and RAM. Interfacing memory to the processor: Address decoding and timing.</td>
<td>29h</td>
</tr>
<tr>
<td><strong>4. The I/O subsystem</strong></td>
<td>I/O interfaces, programmed and interrupt-driven I/O. DMA</td>
<td>22h</td>
</tr>
<tr>
<td><strong>5. Advanced microprocessor systems</strong></td>
<td>Pipelined and superscalar processors. Memory Hierarchy and Cache</td>
<td>16h</td>
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</tbody>
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### Planning of activities

#### LABORATORY

**Description:**
Continuous assessment of laboratory exercises to develop along the course.

#### EXERCISES

**Description:**
Exercises to strengthen the theoretical knowledge.

#### SHORT ANSWER TEST (CONTROL)

**Description:**
Mid term control.

#### EXTENDED ANSWER TEST (FINAL EXAMINATION)

**Description:**
Final examination.

### Qualification system

- Final examination: 60%
- Mid-term exam: 15%
- Laboratory assessments: 25%

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### 6. Software development tools

**Learning time:** 22h
- Theory classes: 4h
- Laboratory classes: 4h
- Self study: 14h

**Description:**
Development process, Integrated development environment, Testing and debugging tools, RTOS
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Bibliography

Basic:


Complementary: