Degree competences to which the subject contributes

Transversal:

1. EFFECTIVE USE OF INFORMATION RESOURCES: Managing the acquisition, structuring, analysis and display of data and information in the chosen area of specialisation and critically assessing the results obtained.
2. FOREIGN LANGUAGE: Achieving a level of spoken and written proficiency in a foreign language, preferably English, that meets the needs of the profession and the labour market.

Learning objectives of the subject

Learning objectives of the subject:

The aim of this course is to train students in including electromagnetic compatibility issues to consider the design of electronic products. First we consider the mandatory international standards and tests. Then, they will study specific electronic design technique to fulfil these requirements.

Learning results of the subject:

- Ability to understand and apply EMC international standards.
- Ability to perform radiated and conducted tests, including ESD, to evaluate electronic designs emissions and immunity.
- Ability to apply specific electronic design techniques to reduce conducted and radiated interferences.
- Ability to apply specific electronic design techniques to improve conducted and radiated immunity.
- Ability to understand and apply International Electromagnetic Compatibility (EMC) standards with special focus in European Directives.
- Ability to plan and perform conducted and radiated EMC tests.
- Ability to design electronic circuits and products taken into account their electromagnetic emission and immunity.
## Study load

<table>
<thead>
<tr>
<th></th>
<th>Hours large group:</th>
<th>Hours medium group:</th>
<th>Hours small group:</th>
<th>Guided activities:</th>
<th>Self study:</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Total learning time</strong>: 125h</td>
<td>13h</td>
<td>0h</td>
<td>26h</td>
<td>0h</td>
<td>86h</td>
</tr>
<tr>
<td></td>
<td>10.40%</td>
<td>0.00%</td>
<td>20.80%</td>
<td>0.00%</td>
<td>68.80%</td>
</tr>
</tbody>
</table>
## Introduction to Electromagnetic Compatibility (EMC)

**Description:**
- EMC Basics Concepts

**Learning time:** 8h
- Laboratory classes: 2h
- Self study: 6h

## Radiated interferences

**Description:**
- Emission sources and radiating elements
- Coupling on cables and PCB
- Shielding

**Learning time:** 36h
- Theory classes: 4h
- Laboratory classes: 8h
- Self study: 24h

## Conducted interferences

**Description:**
- Filtering
- Transient suppression

**Learning time:** 19h
- Theory classes: 2h
- Laboratory classes: 4h
- Self study: 13h

## Transients

**Description:**
- Burst, Surge, ESD
- Protections

**Learning time:** 19h
- Theory classes: 2h
- Laboratory classes: 4h
- Self study: 13h
### PCB design

**Learning time:** 19h  
Theory classes: 2h  
Laboratory classes: 4h  
Self study: 13h  

**Description:**  
Layout design

### EMC standards

**Learning time:** 19h  
Theory classes: 2h  
Laboratory classes: 4h  
Self study: 13h  

**Description:**  
European and International Standards

### Numerical Simulation Techniques

**Learning time:** 5h  
Theory classes: 1h  
Self study: 4h  

**Description:**  
FDTD
# 230673 - EMC - Emc in Electronic Design

## Planning of activities

| Laboratory | Hours: 26h  
<table>
<thead>
<tr>
<th></th>
<th>Laboratory classes: 26h</th>
</tr>
</thead>
</table>
| **Description:** | - EMC measurements  
| | - EMC electronic design techniques |

**SHORT ANSWER TEST (CONTROL)**

| Hours: 1h  
<table>
<thead>
<tr>
<th>Laboratory classes: 1h</th>
</tr>
</thead>
</table>
| **Description:** Mid term control.

**EXTENDED ANSWER TEST (FINAL EXAMINATION):**

| Hours: 2h  
<table>
<thead>
<tr>
<th>Theory classes: 2h</th>
</tr>
</thead>
</table>
| **Description:** Final examination with theoretical questions and short exercises.

## Qualification system

Laboratory assessments: 20%
Partial examinations and controls: 30%
Final examination: 50%

## Bibliography

**Basic:**


**Complementary:**


**Others resources:**